

# Sheryl Perez, ECE, CLSSGB

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## Summary

Accomplished Quality Engineer with 15+ years demonstrated history of working in electronics manufacturing industry, with proven record of significant, successful contribution in quality management, new project/product qualification, market and supplier quality management, manufacturing quality, quality documentations and process improvement. Focus on customer orientation, lean thinking with strong organizational, communication and leadership skills. To engage in a position that utilizes my many years of experience in Quality Control and Quality Assurance and supervision of workgroups.

## Experience

### Quality Manager

Dyson

Apr 2022 - Jun 2023 (1 year 3 months)

In-charge with Dyson's Floor and Hair Care projects (Vacuum Cleaners, Hair Stylers) and manages one of the Contract Manufacturer/Supplier in the Philippines (HI-P PH), to ensure delivery of quality products in compliance to Dyson Quality Standards.

### Quality Manager (Program Quality)

Dyson

Jul 2023 - Present (8 months)

- Oversee all quality functions (Safety, Reliability, Customer Satisfaction, Design, Manufacturing/ Production, Supply) from the early development phase (SD/Solutions Demonstrated) to Market Launch.
- Cascade important project updates to quality functions, plan and ensure individual deliverables/targets are achieved based on project timelines and Milestones
- Represent all quality functions during Project Lead Team meetings and especially on new project's Milestone Delivery/Review
- Lead activities to maintain project's Reliability Targets and Customer Satisfaction Targets
- Drive activities to improve products' Cost of Poor-Quality Performance through TDM (Top Defect Matrix) via 3Ps methodology (Predict, Prevent, Protect)

### Lead Quality Engineer

Dyson

Apr 2019 - Present (4 years 11 months)

In-charge with Dyson's Floor Care projects (Cordless Vacuum Cleaners). Manages the Contract Manufacturer/Supplier in the Philippines, to ensure delivery of quality products in compliance to Dyson Quality Standards.

NPI/Launch Quality

- New Line Qualification: Leads in the qualification of new lines through 5M1E buy-off.

- Documents Readiness: Leads the review and updating of PFMEA, Control Plan, QSI & other quality documents with Dyson and CM cross-functional teams during qualification builds.
- Part Approval: Pre-reviews Dyson Part Approval Process (DPAP) requirements for sub and top-level assembly prior RDD review and approval.
- Start of Production Sign Off: Responsible for signing off CM's SOP according to 5M1E deliverables readiness.

#### Sustaining Quality

- CM Compliance: Ensure CM's internal procedures are align with DQS.
- Quality Metrics: Leads regular review of CM's KPI for IQC/IPQC/OQC/OBA, to ensure Dyson thresholds are met including Yield/RTY, 8D, SPC reviews, etc.
- Control Plan Compliance: Conducts regular control plan audit to ensure drawing requirements (CNs/ECO) are consistently updated on CM quality docs and controls are effectively implemented.
- Supplier Scorecard: Create CM Score-Card based on internal & external performance. Communicate CM Quality performance to Top Management through Quality Metrics data & audit results.
- Risk Assessments: Reviews Deviation Report, rework flows & risk assessments for any mfg. issues to gain confidence prior giving green light to build and ship.
- Lessons Learnt/Continuous Improvement: Determine gaps & create plans to close it against Dyson agreed strategy for CM capabilities and performance. Review and apply LLs.

#### Market Quality

- Communicate/liaise with market team to give timely update on 8Ds for Field Failures related to CM mfg. process.
- Support, assist, coordinate market rework as required.
- Work w/ market team to share early warning signals that may help to review and improve mfg. controls as needed.



### Quality Engineer-Advanced

Dyson

Jun 2017 - Mar 2019 (1 year 10 months)



### Quality Engineer

Dyson

Nov 2016 - Jun 2017 (8 months)

### Process Engineer (Supervisor)

EMS Components Assembly Inc.

Jul 2016 - Nov 2016 (5 months)

- Supports in the qualification of new PCBA models and lines for Toshiba through buy-off.
- Reviews and revise PFMEA and Work Instructions for any needed updates.
- Ensure SMT line is running according to process specifications through daily process audits.
- Leads the investigation and reports of PCBA line fall-outs.

### Customer/Joint Quality Senior Engineer

SMART ELECTRONICS MFG. SERVICES CORP. PHIL. (formerly SAMSUNG ELECTRONICS PHILS. MFG. CORP.(SEPHIL)

2015 - May 2016 (1 year)

- Acts as the main window person to Toshiba as a subcontractor company for HDD PCB assembly.

- Handles the qualification of new model of HDD PCB assembly (SMT) and other new products or models.
- Ensures HDD manufacturing quality through monitoring of quality control plans and periodic audits.
- To lead by initiating closed loop corrective action, stop production/shipment, product recall to customers.
- Monitor quality trend on SMT lines, suggest and spearhead continuous improvements of manufacturing process and procedure in terms of quality, cost and productivity along with other departments.
- Facilitates customer complaints, non-conformance issues, customer interfaces and provide 8D/CLCAR Report.
- Review Engineering documentations such as Work Instructions, FMEA, OCAP, 5-Why and Control Plans for related process.
- Approval on special order/5M1E/ECN changes.
- Conduct internal QSA (Quality System Audit) and QPA (Quality Process Audit) based on customer requirements and criteria.

### **Market Failure Analysis Senior Engineer**

SMART ELECTRONICS MFG. SERVICES CORP. PHIL. (formerly SAMSUNG ELECTRONICS PHILS. MFG. CORP.(SEPHIL)

Jan 2014 - Oct 2015 (1 year 10 months)

- Responsible in monitoring market quality trends and issues.
- Confirms market rejects and conduct failure analysis through the use of advance tools (oscilloscope, digital wave analyzer). Coordinates to supplier, production and RDD for endorsements of failed ODDs upon 1st level FA root cause identification.
- Review and monitors CLCA and Failure Analysis Turn Around Time.
- Provide Customer On-Site FA support if necessary.
- Administers project documentation such as market quality, customer requirement data and FA Reports.

### **Read Servo Engineer**

SMART ELECTRONICS MFG. SERVICES CORP. PHIL. (formerly SAMSUNG ELECTRONICS PHILS. MFG. CORP.(SEPHIL)

Jun 2013 - Jan 2014 (8 months)

Drive Development/Research and Development Department

- Design, review and Debug Firmware Codes according to Hardware specifications
- Conduct failure analysis and make technical reports related to product performance like mainline production DFA defect, OEM qualification issues (Dell, HP, Toshiba, Asus, Lenovo etc), development issues (Readability, Cert, Flexstar, RDT etc, JQ and OEM qual)
- Review the mainline development production DFA CPK and make countermeasure for fail CPK
- Follow-up and close Readability, Disc ability, and Reliability issues requested by related departments
- Verify Product performance with Standard test tools, equipment, and checklists
- Design Drive Self Test Program Specifications after Review of Productivity CpK

### **Write Strategy Engineer/Quality Assurance Engineer**

SMART ELECTRONICS MFG. SERVICES CORP. PHIL. (formerly SAMSUNG ELECTRONICS PHILS. MFG. CORP.(SEPHIL)

Nov 2008 - Jun 2013 (4 years 8 months)

- Make a write strategy for all media formats (CD, DVD, BD, M-DISC) for ODD compatibility.
- Perform evaluations, qualifications and checklists before media write development stage of ODD in order to reduce or eliminate unnecessary tests flows.
- Create media write qualification plan during new project development.
- Conduct failure analysis for Media write failures both at development and customer side.
- Develops and co-work with projects related to cost reductions, tests systems or processes, techniques and quality improvement.
- In-charge of maintaining, repair & calibration of CAT machines. Creates new templates for module testing, based from customer specifications.
- Facilitate training on basic principles of write strategy, CAT machine handling, calibration, maintenance to new engineers.
- Deployed in TSST (Toshiba-Samsung Storage Technologies) Design Center in Korea to support ODD new projects under development.

## Education



### **Bulacan State University**

Bachelor of Science in Electronics and Communications Engineering

2003 - 2008

## Licenses & Certifications

**ISO 9001:2015 Internal Auditor**

**ISO 45001: 2018 Occupational Health and Safety Management Systems Certified**



**Lean Six Sigma Green Belt - Whitehall Consulting**

## Skills

Firmware debug and simulation skills using C++ Programming source • Design of Experiments • Failure Analysis • Lean Six Sigma • 8D Problem Solving • Failure Mode and Effects Analysis (FMEA) • MSA • Statistical Process Control (SPC) • Microsoft Office